

付録A 回路図

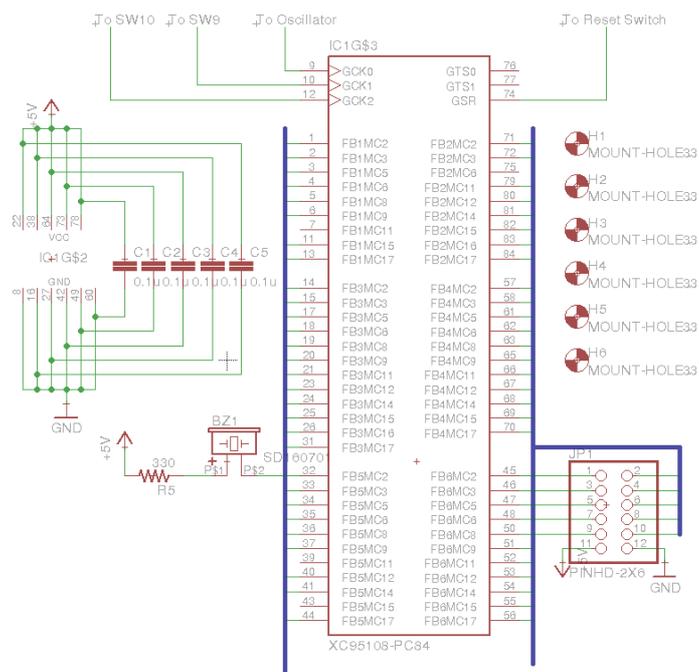


図 A.1: 回路図 1

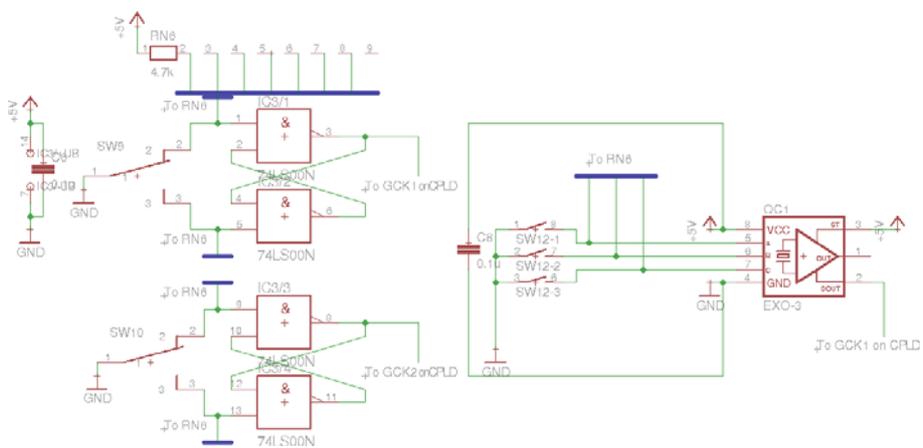


図 A.2: 回路図 2

